


TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD OF FORMING A LOW VOLTAGE GATE OXIDE LAYER AND TUNNEL OXIDE LAYER IN AN EEPROM CELL		
Application Number :	10/717149		
Date :	2003-11-18		
First Named Applicant:	Renninger Alan L.		
Confirmation Number:	6780		
Attorney Docket Number:	ATM-262		
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